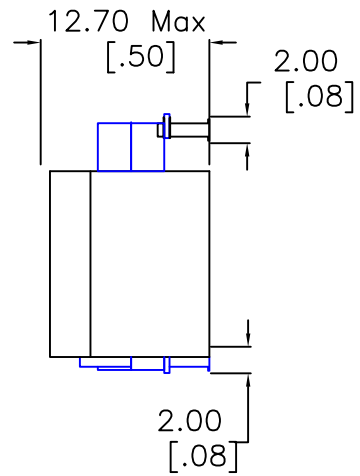
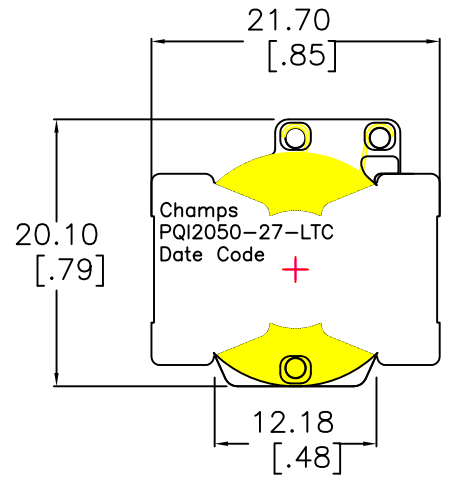
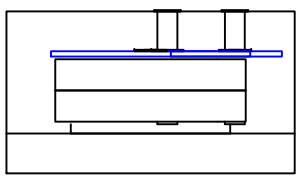
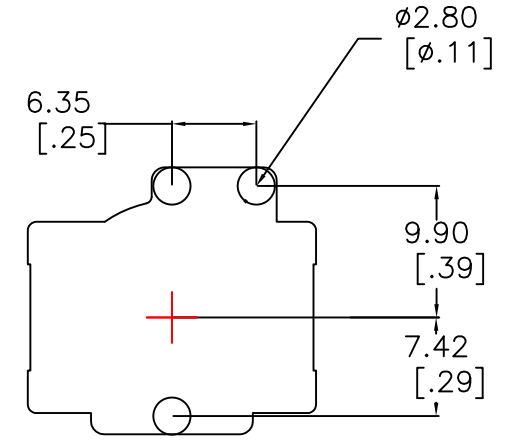


1 2 3 4 5 6 7 8

A



SUGGESTED PAD LAYOUT



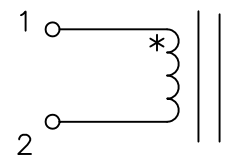
B

C

D

E

Schematic



INDUCTANCE [1-2] = 27.0uH Nom, 24.3 Min. @10kHz/0.1V 8.0 Adc
 INDUCTANCE [1-2] = 21.6 uH Min @10kHz/0.1V 9.8Adc
 DCR [1-2] = 8.70 mohms Nom, 9.6 Max
 DIELECTRIC ISOLATION > 500 VDC [1-2] : CORE
 SATURATION CURRENT @25C = 9.8Adc | @85C = 8.8Adc
 HEATING CURRENT FOR 40C RISE AT 25C AMBIENT = 15 Adc

No.		DESCRIPTION		REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION						
CHAMPS TECHNOLOGIES						
TOLERANCES +/- 1.0 UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs No. PQI2050-27-LTC		
.XXX ±	CHKD	DK	8/20/08	Customer	INDUCTOR	ISSUE A
.XX ±	APPR	HE	8/27/13	Part #:	SCALE 2:1	REV 00
.X ANGLE ±			SIZE			